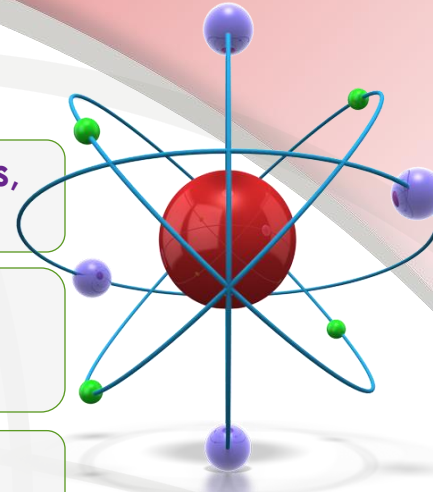


# Silicon Fabrication Process & Device Design Workshop

18-20 March 2014, ICmic Design Centre, SME2, Cyberjaya



**Who?** Academicians, researchers, undergraduates, semiconductor engineers, process technologists, and managers of semiconductor industry.

**What?** Hands-on simulation of silicon devices and technology with SILVACO TCAD Framework. Develop silicon device simulation model and characterization and analysis of the simulated device

**Why?** Enhance competency in process and physical device simulation with SILVACO TCAD suite.

## Registration Fees

RM2500 / USD760 /Person

University / Research Institute:

RM1500 / USD470 /Person

15% Early bird discount (before 20 February 2014)

Group discount is available (3 or more)

## Payment Method

Cheque/Money Order/Bank Draft to IC Microsystems Sdn Bhd  
or

Bank-In MBB A/C : 5624 4180 0285

Fees are NON REFUNDABLE upon registration.

Substitution of participant is allowed.

**CLOSING DATE: 10 March 2014**

18  
Mar

MOSFET Process & Device Simulation

ATHENA process simulator, ATLAS device simulator, Deckbuild interface

19  
Mar

BJT Process & Device Simulation

Process and device simulation of Bipolar Junction Transistor

20  
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Advanced Characterization and Device Editor

Structure editing with device editor, Transient/AC Analysis of Silicon Device

For enquiries/registration, submit to:

IC Microsystems Sdn Bhd

Unit B-G-03, SME Technopreneur Centre 2  
2260, Jalan Usahawan 1

63000 Cyberjaya, Selangor D.E. MALAYSIA

Tel: +603 8319 1919 Fax: +603 8319 1918

Email: info@icmic.com

## Registration Form

Name : \_\_\_\_\_

Institution: \_\_\_\_\_

Address : \_\_\_\_\_

Job Title : \_\_\_\_\_

H/P No : \_\_\_\_\_

Signature : \_\_\_\_\_ Date : \_\_\_\_\_